



Reliable Flash-Backed Cache Using SuperCaps

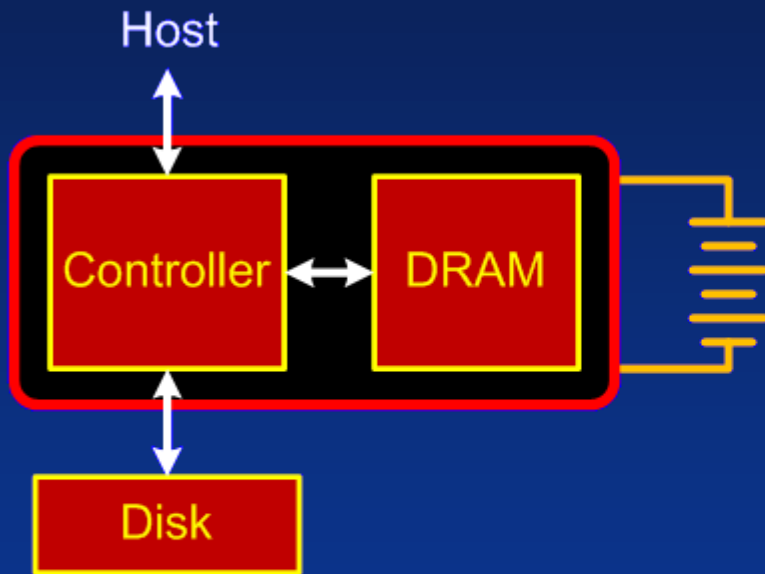
Lane Hauck
AgigA Tech



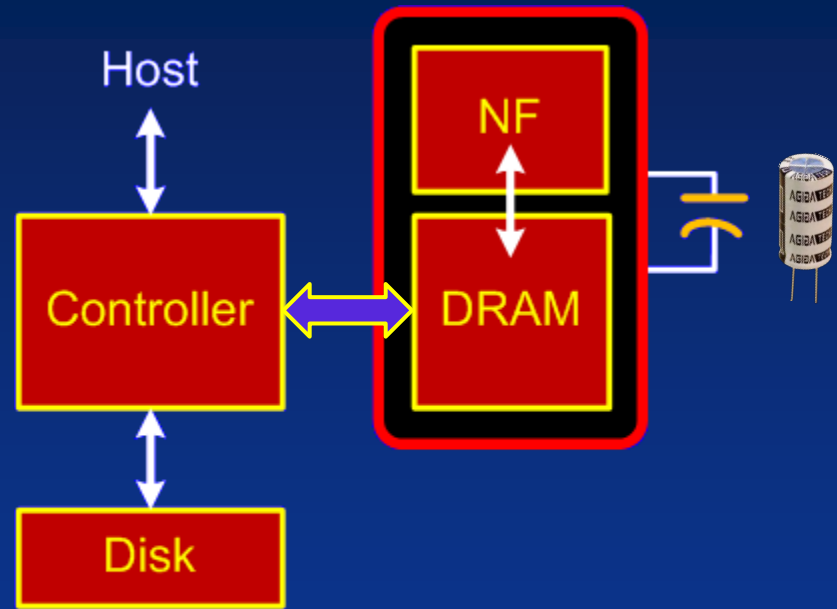
Agenda

- NV cache methods
 - Battery-Backed DRAM
 - NAND Flash (NF)-backed DRAM
- New power source: UltraCaps
 - How they work (+Demo)
 - Test Data
- In-System Advantages
 - Ultracap charge/discharge curves
 - Unmanaged NF
- Summary

Battery-Backed RAM and Flash-Backed RAM



Battery-Backed



NF-Backed

Capacitor Evolution

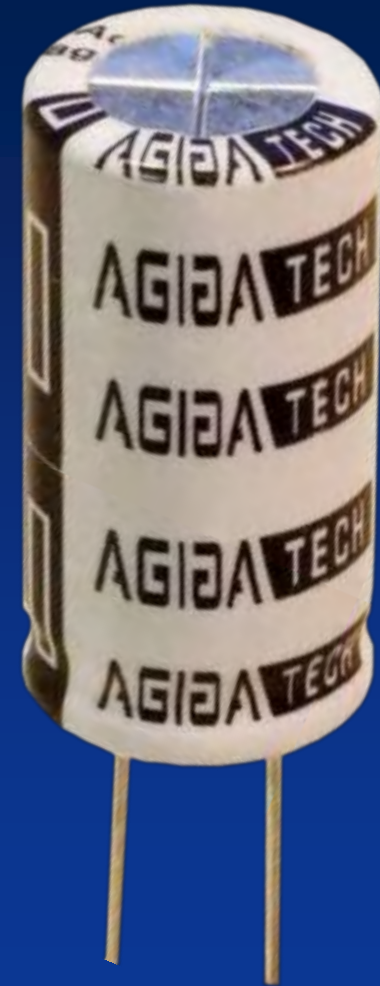
0.001 Farad



~4X Volume...
33,000X Capacitance



33 Farad



How is this possible?

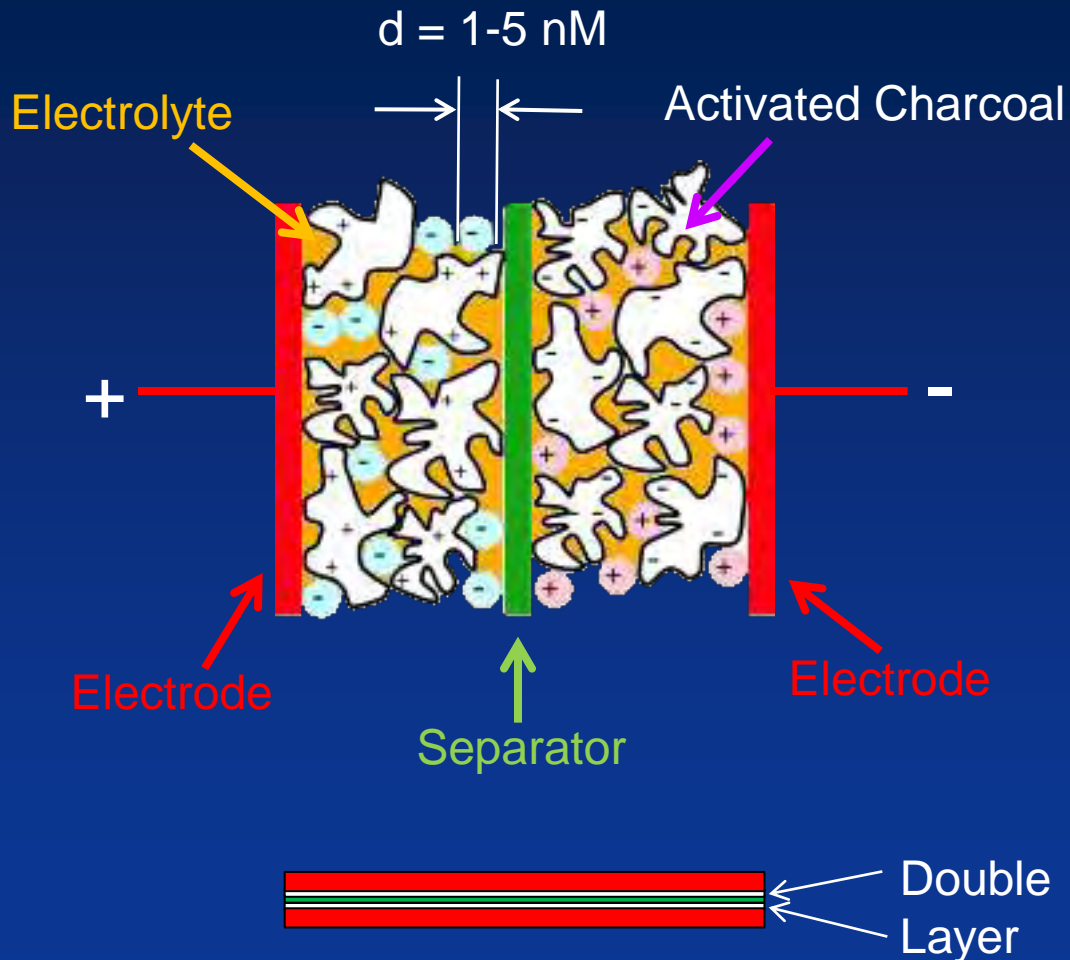
Standard Capacitor



$$C \propto \frac{A}{d}$$

Electrochemical Double-Layer Capacitor (UltraCap)

$$C \propto \frac{A}{d}$$



$A = 1\text{K}-2\text{K} \text{ m}^2/\text{g}$

Demonstration



UltraCap Reliability Testing



Supercap Test Board

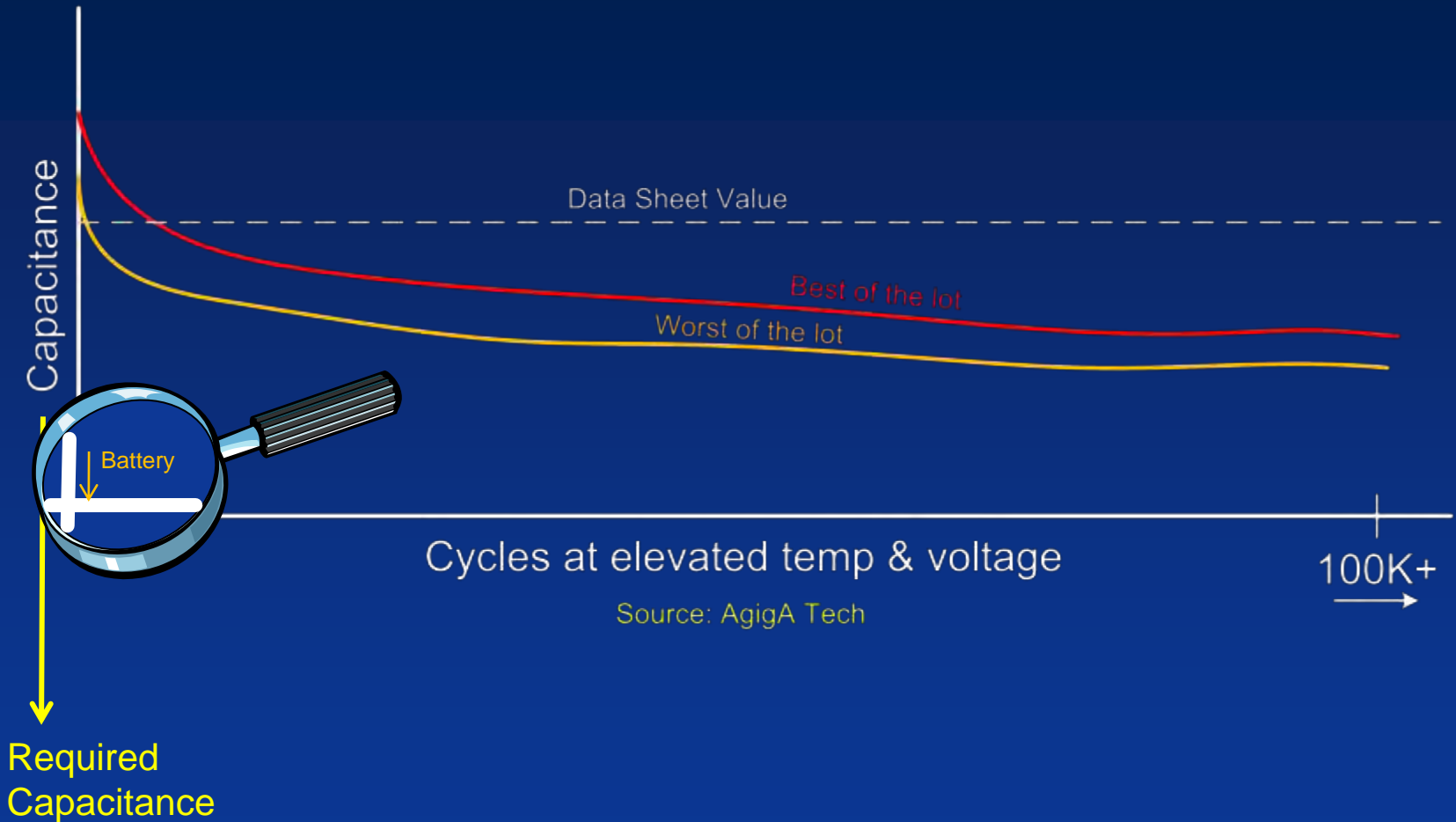


Temp Chamber

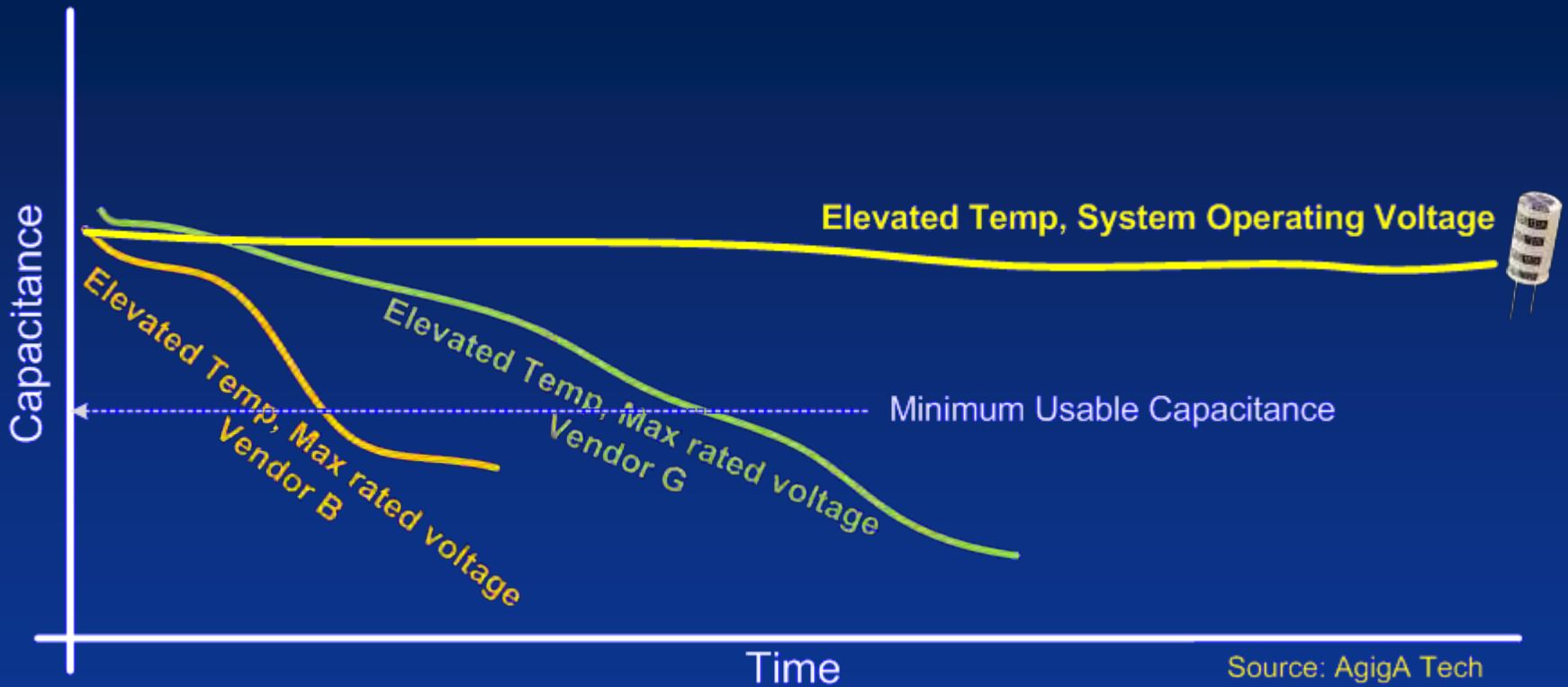


Full System Testing

Charge/Discharge Cycles

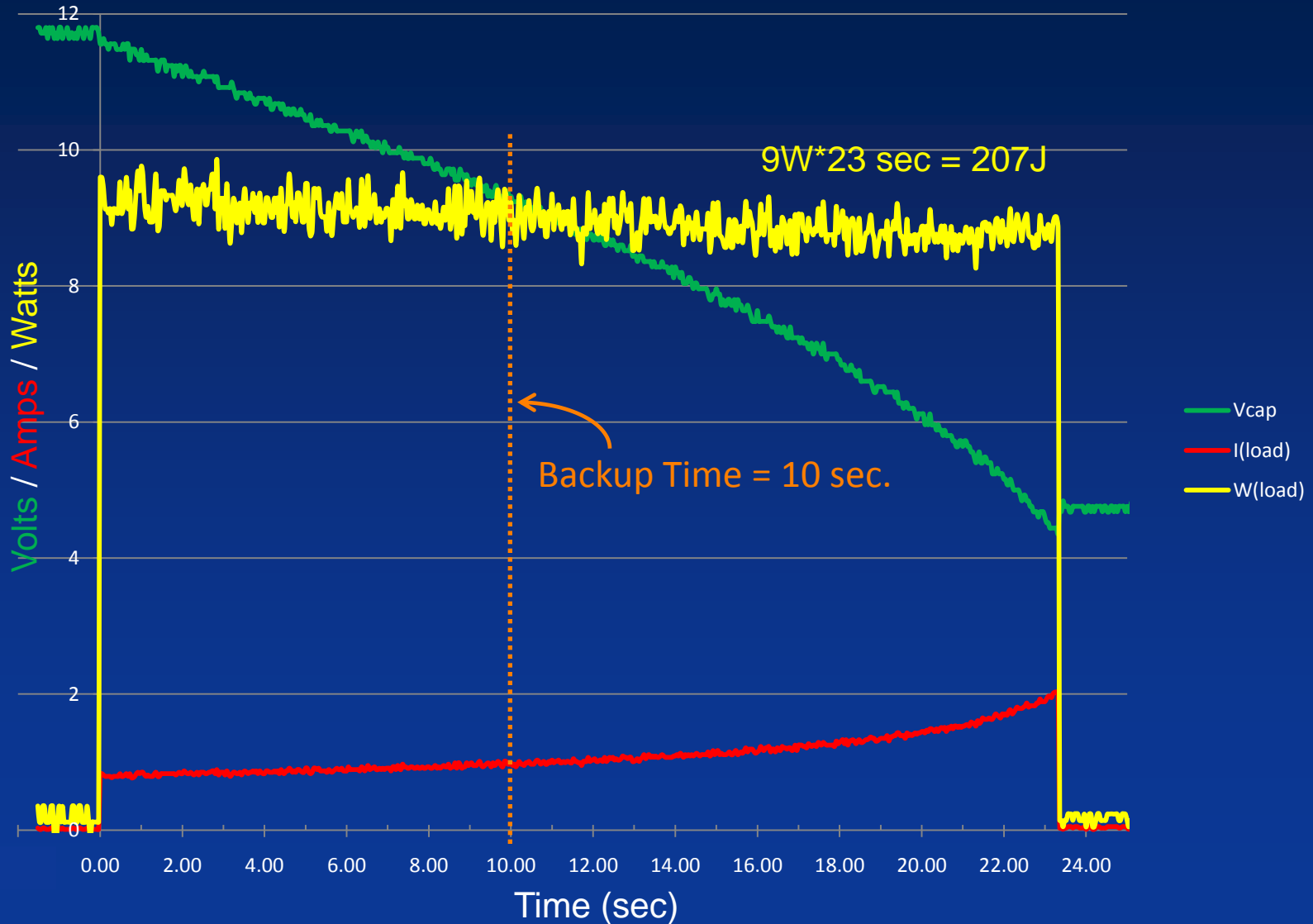


Voltage & Vendors



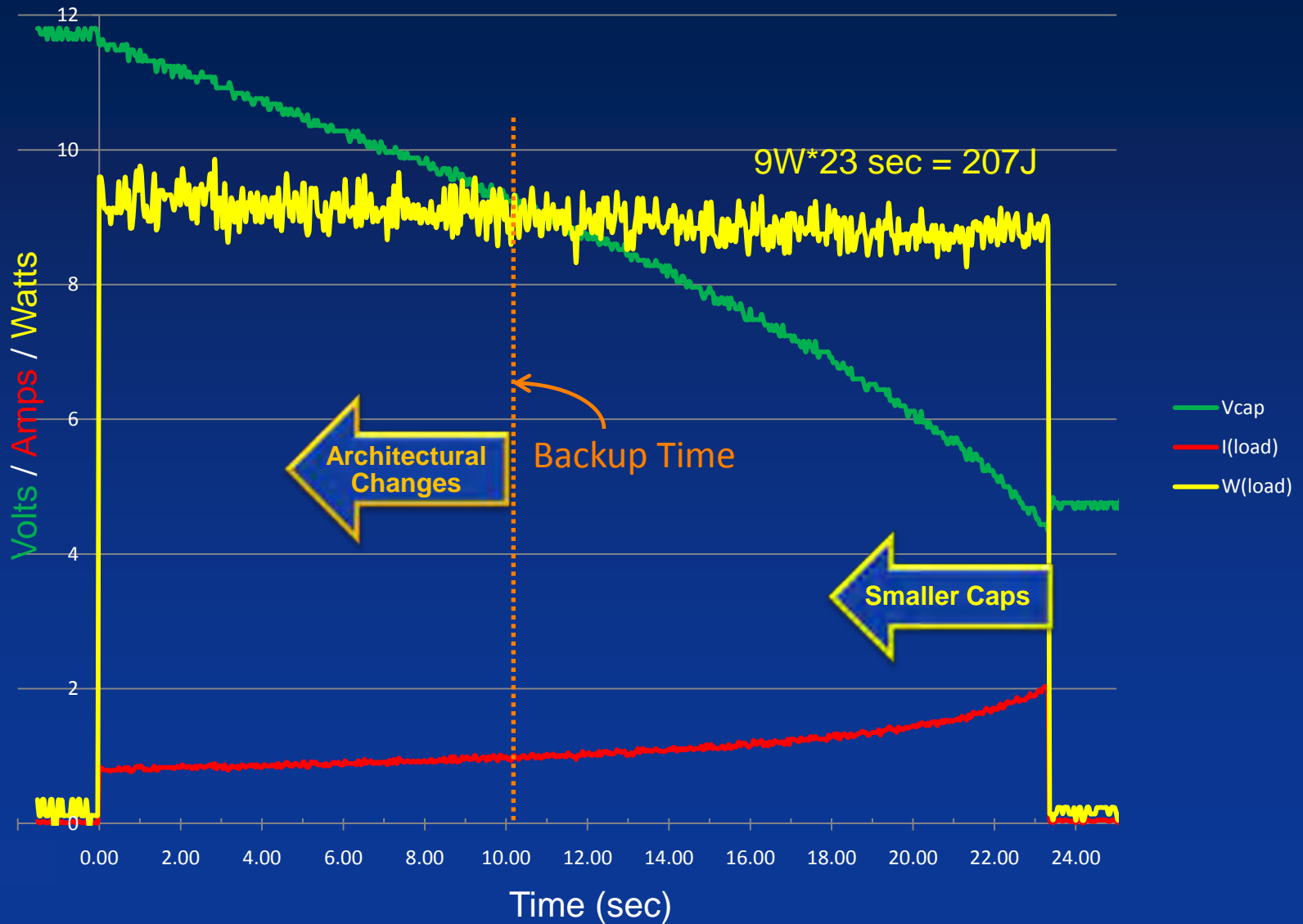
“All data taken at same elevated temp,
well above spec limit.”

In-System PowerGEM™ Energy Measurement

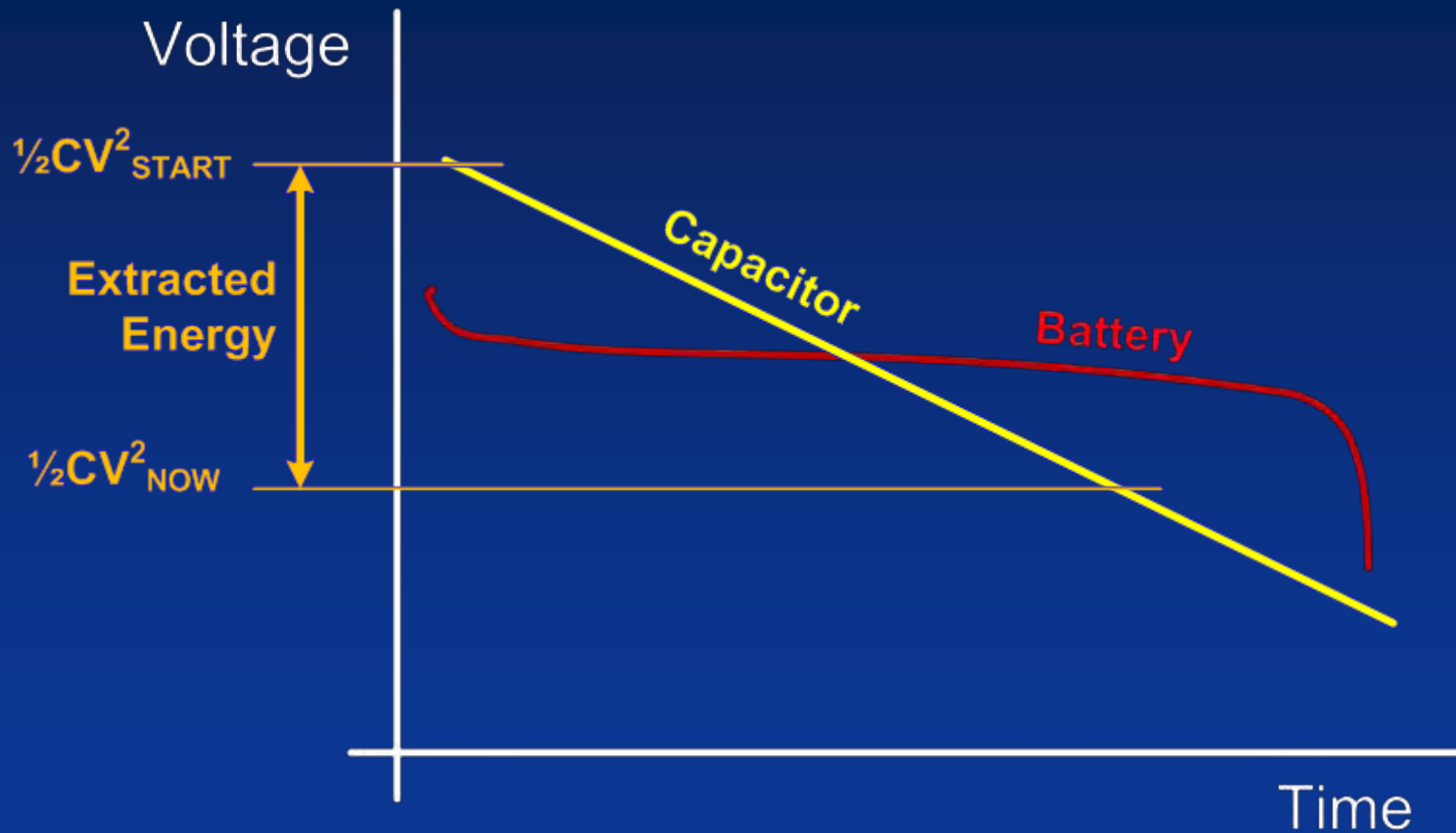


Flash Memory Tradeoffs

Flash Memory SUMMIT



Reliability: How Much Energy?





Other Reliability Issues

- **Unmanaged Flash Has Advantages**
 - Low-level visibility can give early warning
 - Wear tracking can be made available to host
 - Trending over service lifetime
- **SuperCap-powered system remains powered when host loses power**
 - Safety Interlock Signals
 - Host can glitch signals during power up/down
 - Qualifiers (Enables) require normally-operating host
 - Complex system readiness reduced to single GTG (Good To Go) signal
 - Multiple readiness factors readable over I²C registers

- NF-backed SDRAMs make excellent reliable power-loss protected caches
 - High densities, e.g 1GB-8GB
- UltraCaps are ideally suited to this application
- UltraCaps wear, but not a system issue if properly selected, sized and rated
- Fine control over system “internals” like managing the NF allows precise health monitoring and tracking
- Special attention is required to operate a powered system while its “master” loses power