

Memristive Memory: a fundamental shift



Janice Nickel, PhD Nanoelectronics Research Group, Hewlett Packard Laboratories Flash Memory Summit, August 10, 2011



The Memristor: Predicted

Fourth Fundamental Two Terminal Circuit Element





The Memristor: Fundamentally Different

Dynamical Non-Linear Behavior



Generalized Memristor (Memristive system):

v = M(w,i)i

$$\frac{dw}{dt} = f(w, i)$$

L. Chua and S. M. Kang, Proceedings of the IEEE, Vol. 64, No. 2, February 1976





The Memristor: Found

Reduced to Practice in 2008





R. Stanley Williams HP Laboratories

D. B. Strukov, et al., vol 453, 1 May 2008, doi:10.1038/nature06932





What makes a memristor "fundamental"?

Inability to duplicate properties with the other passive circuit elements











How does it work?

Semiconducting Bipolar Switch



Previously: Now:

Fixed semiconductor structure and only electronic motion lonic motion dynamically modulates the semiconductor structure controlling the electronic current.







How does it stand up as a memory?

Dynamical and Non-Linear – Enables True Cross-point

	Memristor	PCM	STTRAM	DRAM	Flash	HDD
Density (F ²)	4	8–16	14–64	6–10	4–6	2/3
Energy per bit [†] (pJ)	1–3	2–27	0.1	2	10000	1–10x10 ⁹
Read time (ns)	<10	20–70	10–30	10–50	25000	5-8x10 ⁶
Write time (ns)	~.20	50-500	13–95	10–50	200000	5-8x10 ⁶
Retention	years	years	weeks?	< <second< th=""><th>years</th><th>years</th></second<>	years	years
Endurance (cycles)	>10 ¹²	10 ⁷	10 ¹⁵	10 ¹⁶	10 ³ - 10 ⁶	10 ¹⁵



Thank you



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